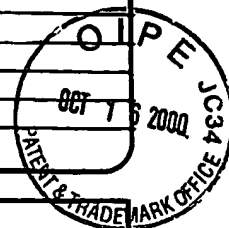


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				Application Number	09/583,386
				Filing Date	May 30, 2000
				First Named Inventor	Carley
				Group Art Unit	2813
				Examiner Name	KIELIN
				Attorney Docket Number	000265
Sheet	1	of	3		

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George M. K.

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¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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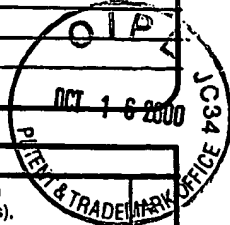
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Application Number	09/583,386		
		Filing Date	May 30, 2000		
		First Named Inventor	Carley		
		Group Art Unit	2812		
		Examiner Name			
Sheet	2	of	3	Attorney Docket Number	000265



OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS		
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
ER		G.K. FEDDER, S. SANTHANAM, M.L. REED, S.C. EAGLE, D.F. GUILLOU, M.S.-C LU, and L. RICHARD CARLEY, Laminated High-Aspect-Ratio Microstructures in a Conventional CMOS Process, 9th IEEE International Workshop on Micro Electro Mechanical Systems, San Diego, CA USA, Feb. 11-15, 1996.
ER		G.K. FEDDER, S. SANTHANAM, M.L. REED, S.C. EAGLE, D.F. GUILLOU, M.S. C. LU AND L. RICHARD CARLEY, Laminated High-Aspect-Ratio Microstructures in a Conventional CMOS Process, Sensors & Actuators A, vol. 57, No. 2, March 1997, pp. 103-110.
ER		G.K. FEDDER, Integrated Microelectromechanical Systems in Conventional CMOS, 1997 IEEE Int. Symposium on Circuits and Systems, Hong Kong, June 9-12, 1997, v. 4, pp. 2821-2824
ER		G.K. FEDDER, CMOS-Based Microstructures with Lateral Electrostatic Actuation, 1996 Sensors Expo Philadelphia Conf., Philadelphia, PA October 22-24, 1996, pp. 131-135.
ER		D.A. KOESTER, R. MAHADEVAN, B. HARDY and K. MARKUS, MUMPs Design Handbook, Cronos Integrated Microsystems, N.C., rev. 3, Oct. 1994.
		Sandia National Laboratories, Intelligent Micro Machine Initiative.
ER		J.H. SMITH, S. MONTAGUE, J.J. SNIEGOWSKI, J.R. MURRAY and P.J. McQHORTER, Embedded Micromechanical Devices for the Monolithic Integration of MEMS with CMOS, IEDM, 1995, pp. 609-612.
ER		M. PARAMESWARAN, A.M. ROBINSON, D.L. BLACKBURN, M. GAITAN and J. GEIST, Micromachined Thermal-Radiation Emitter from a Commercial CMOS Process, 1991 IEEE, Vol. 12, No. 2, pp. 57-59

Examiner Signature	<i>Eric K. ...</i>	Date Considered	5/30/00 8/11/00
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	AI					
	AJ					
	AK					

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
	AL	EP 0 624 900 A2	04/18/94	European		X
	AM					
	AO					
	AP					

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

AR	Gianchandani, Y.B., Kim, H., Shinn, M., Ha, B., Lee, B., Najafi, K. and Song, C., "A MEMS-First Fabrication Process for Integrating CMOS Circuits with Polysilicon Microstructures", 1998 IEEE, pgs. 257-262
AS	Kniffin, Margaret L., Shah, Mahesh, "Packaging for Silicon Micromachined Accelerometers", The International Society for Hybrid Microelectronics, Volume 19, Number 1, First Quarter 1996, pgs. 75-86
AT	

EXAMINER

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	AK						

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	AT	

EXAMINER

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